



09/637,532

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Dmitriy Rumynin et al.

Examiner: David H. Malzahn

Serial No.: 09/637,532

Group Art Unit: 2124

Filed: August 11, 2000

Docket: 1365.033US1

Title: A PARALLEL COUNTER AND MULTIPLICATION LOGIC CIRCUIT

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT Technology Center 2100

Assistant Commissioner for Patents

P.O.Box 1450

Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p). Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

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Respectfully submitted,

DMITRIY RUMYNIN ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

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Date 10 Nov '03By [Signature]

Timothy B. Clise

Reg. No. 40,957

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[Signature]
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Signature



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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Use as many sheets as necessary)

Complete if Known

Application Number	09/637532
Filing Date	August 11, 2000
First Named Inventor	Rumynin, Dmitriy
Group Art Unit	2124
Examiner Name	Malzahn, David

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Sheet 1 of 2

Attorney Docket No: 1365.033US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-2002/0026465	02/28/2002	Rumynin, D, et al.	708	210	01/25/2001
	US-2002/0078110	06/20/2002	Rumynin, D, et al.	708	210	07/27/2001
	US-3,634,658	01/11/1972	Brown, Richard	235	92LG	03/19/1970
	US-3,757,098	09/04/1973	Wright, Carl	235	175	05/12/1972
	US-4,607,176	08/19/1986	Burrows, James, et al.	307	449	08/22/1984
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	US-6,023,566	02/08/2000	Belkhale, K., et al.	395	500.03	04/14/1997

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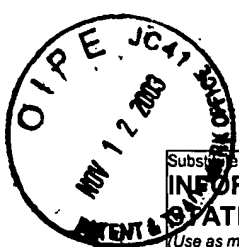
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
	EP-0168650	01/22/1986	Darringer, J. , et al.	G06F	15/60	
	EP-0309292	03/29/1989	Nishiyama, T. , et al.	G06F	15/60	
	EP-0442356	08/21/1991	Chang, Yen C.			
	FR-2475250 with English abstract	08/07/1981	Houdard, Jean-Pierre , et al.	606F	7/38	
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	WO-02/12995	02/14/2002	Meulemans, P	G06F	7/00	
	WO-99/22292	05/06/1999	Verbauwhede, Ingrid			

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		CHAKRABORTY, S. , et al., "Synthesis of Symmetric Functions for Path-Delay Fault Testability", <u>12th International Conference on VLSI Design</u> , (1999),pp. 512-517	
		DEBNATH, D. , "Minimization of AND-OR-EXOR Three-Level Networks with AND Gate Sharing", <u>IEICE Trans. Inf. & Syst.</u> , E80-D, 10, (1997),pp. 1001-1008	
		DRECHSLER, R. , et al., "Sympathy: Fast Exact Minimization of Fixed Polarity Reed-Muller Expressions for Symmetric Functions", <u>IEEE</u> , (1995),pp. 91-97	
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DATE CONSIDERED



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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
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Application Number 09/637532

Filing Date August 11, 2000

First Named Inventor Rumynin, Dmitriy

Group Art Unit 2124

Examiner Name Malzahn, David

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Sheet 2 of 2

Attorney Docket No: 1365.033US1

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

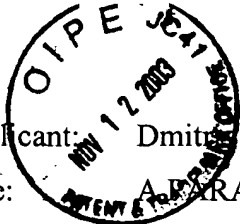
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Operations", IBM Research Center, RC-289, Research Report, (July 18, 1960), 22 pages	
		GOTO, et al., "A 54 x 54-b Regularly Structured Tree Multiplier", IEEE Journal of Solid-State Circuits, Vol 27, No. 9, (Sept. 1992), 1229-1236	
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		HO, I., et al., "Multiple Addition by Residue Threshold Functions and Their Representation By Array Logic", The Institute of Electrical and Electronics Engineers, Inc., Reprinted, with permission from IEEE Trans. Comput. C-22: 762-767, (1973), pp. 80-85	
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EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached



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2124\$

Applicant: Dmitri Rumynin et al.

Title: A PARALLEL COUNTER AND MULTIPLICATION LOGIC CIRCUIT

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Examiner: David H. Malzahn

Customer No.: 21186

Serial No.: 09/637,532

Due Date: N/A

Group Art Unit: 2124

Confirmation No.: 7272

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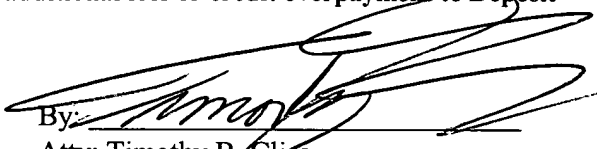
We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ An Supplemental Information Disclosure Statement (1 pg.), Form 1449 (2 pgs.), and copies of 32 cited documents.
- ☒ A check in the amount of \$180.00 to cover the fee for consideration of Information Disclosure Statement under 97(c).

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

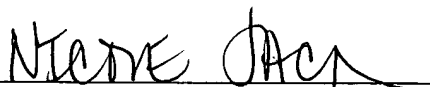
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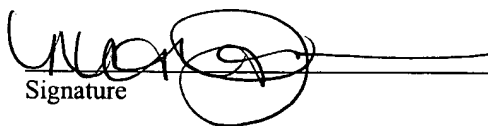
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Atty: Timothy B. Clise
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